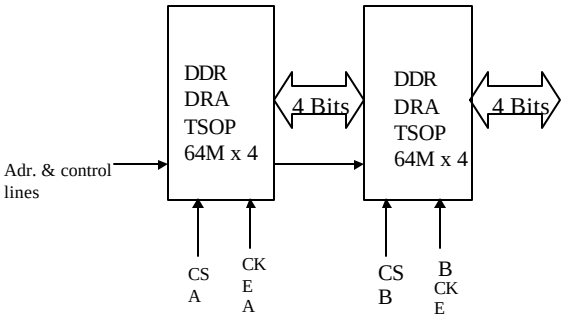
Irvine Sensors Corporation
Microelectronics Products Division

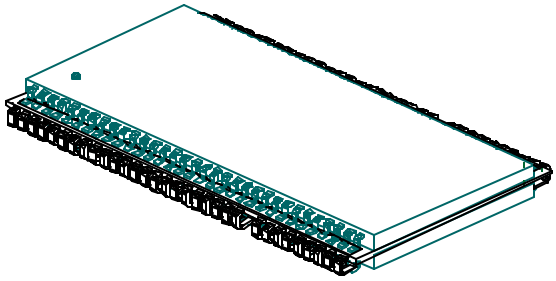
512Mbit (128M x 4) DDR DRAM Memory Stack

Features:

- Low Profile: same PCB footprint as a monolithic TSOP
- Provides a cost-effective emulation of a 512 Mbit DDR DRAM by using 2 low cost 1256 Mbit DDR DRAMs.



- Low cost, economical for volume commercial applications
- JEDEC Standard 3.3V power supply



Dimensions: 0.890”L x 0.525”W x 0.100”H
(+/- 0.005” on all dimensions)

General Description

The Irvine Sensor’s Microelectronics Products Division's 512Mbit DDR DRAM memory stack provides a cost-effective interim alternative to the monolithic 512Mbit DDR DRAM. Boards can be designed to accept either the monolithic 256Mbit TSOP or the 512Mbit stack interchangeably. This allows for a generation upgrade without a board redesign. When the monolithic 512Mbit TSOP becomes more cost effective than the 256Mbit based stack, minor modification to the board design can make it compatible with either the monolithic 512Mbit TSOP or the 1Gbit stack. Base design uses Samsung K4H560438 TSOPs but these stacks can be manufactured using customer furnished TSOPs from a variety of manufacturers.

Application note: Do not exceed 220°C when soldering part to PCB.

For more information contact MPD Sales:
Phone 1-714-549-8211 Fax 1-714-444-8764 E-mail mpdsales@irvine-sensors.com
Irvine Sensors Corporation, 3001 Redhill Avenue, Building 3, Costa Mesa, CA 92626-4532
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